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APPLICATION NO.	FILING DATE	FIRST NAMED INVE		ENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/812,403	03/30/2004	03/30/2004 Yasumasa Tsukan			67161-149	1709
TITLE OF INVENTION: SEMICONDUCTOR MEMORY DEVICE WITH BACK GATE POTENTIAL CONTROL CIRCUIT FOR TRANSISTOR IN MEMORY CELL						
APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400		\$300	\$1700	06/02/2006
EXAMINER		ART UN	Т	CLASS-SUBCLASS]	
AUDUONG, GENE NGHIA		2827	2827 3			
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